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S. Cotton
12-3-02

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AMENDMENTPATENT

TOWNSEND and TOWNSEND and CREW LLP

By: 

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Earle W. Jennings, III et al.

Application No.: 08/993,442

Filed: December 18, 1997

For: VIDEO FRAME RENDERING
ENGINE

Examiner: Chuong D. Ngo

AMENDMENT**Box RCE**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the final office action mailed June 24, 2002, please amend this application as follows:

IN THE CLAIMS:

Please amend claims 1 and 10 as indicated. Please add new claims 21-24.

Surie G

1. (Amended) An integrated circuit for image frame rendering and
2. DSP applications, the integrated circuit during operation operating with memory, the
3. integrated circuit comprising:
4. an interface circuit configured to control access to said memory, the
5. interface circuit coupled to said memory;